Electronic Acknowledgement Receipt				
EFS ID:	2467898			
Application Number:	10707071			
International Application Number:				
Confirmation Number:	1070			
Title of Invention:	AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS			
First Named Inventor/Applicant Name:	Darren L. Anand			
Customer Number:	23389			
Filer:	Steven Fischman/Grace Colucci			
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Attorney Docket Number:	BUR920030168US1			
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File Listing:								
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)			
1 Request for Continued Examination (RCE)	•	BUR920030168US1_Reque stforContinuedExamination.p	727793	no	3			
	df	06a024e20cb3cc0a0c6fb546ffd8b6c5e 29a23c6						
Warnings:								
Information:								
2 Fee Worksheet (PTO-06)	fee-info.pdf	8218	no	2				
		fa01b8724dce7d951e2e1bb07835144b 4605ad25						
Warnings:								
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		Total Files Size (in bytes):	736011					
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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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